

BEST AVAILABLE COPY

1/17

FIG. 1

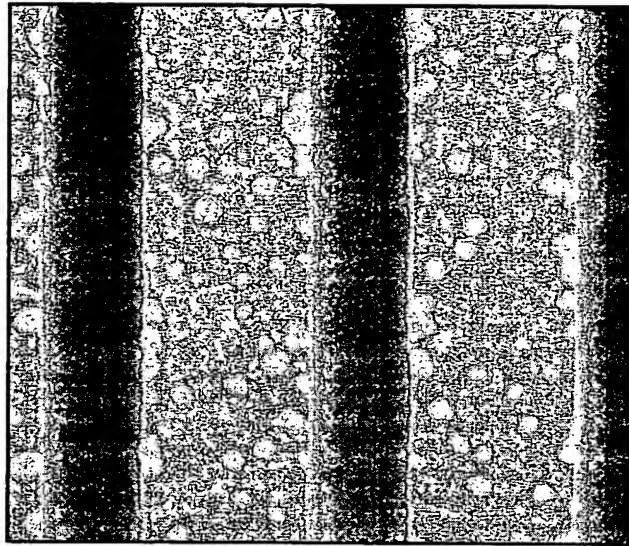
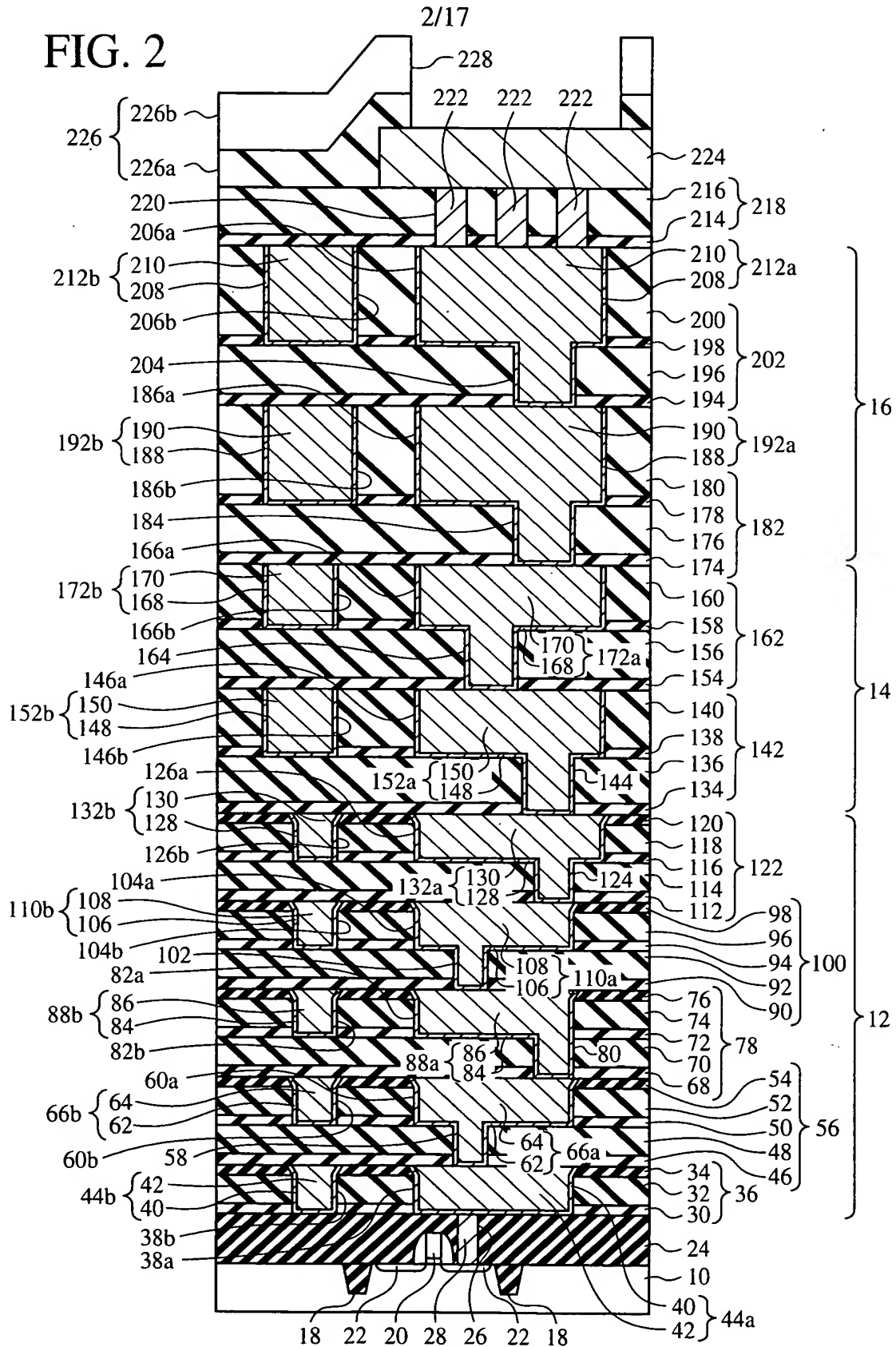


FIG. 2



3/17

FIG. 3A

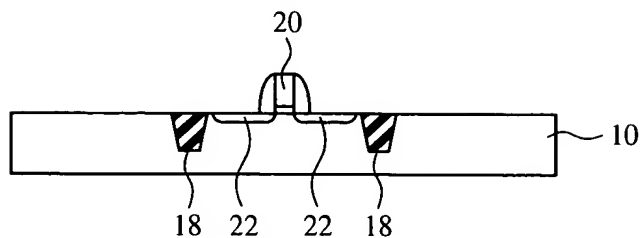


FIG. 3B

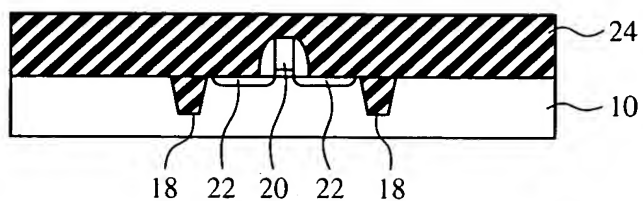


FIG. 3C

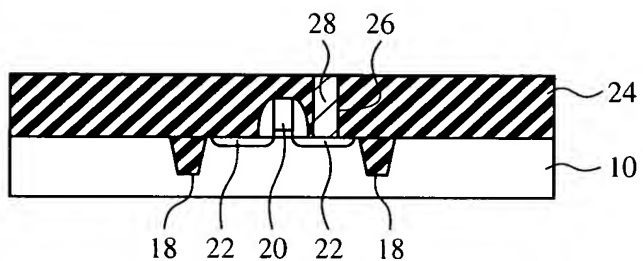


FIG. 3D

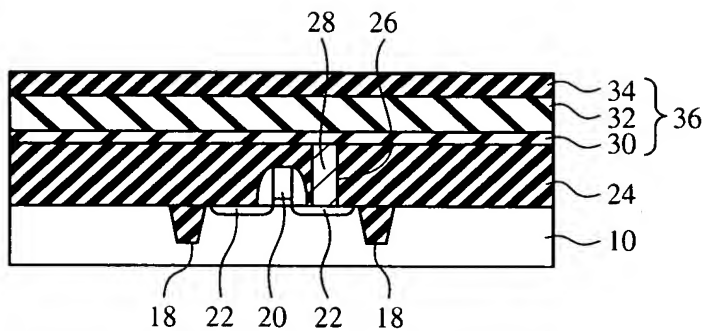
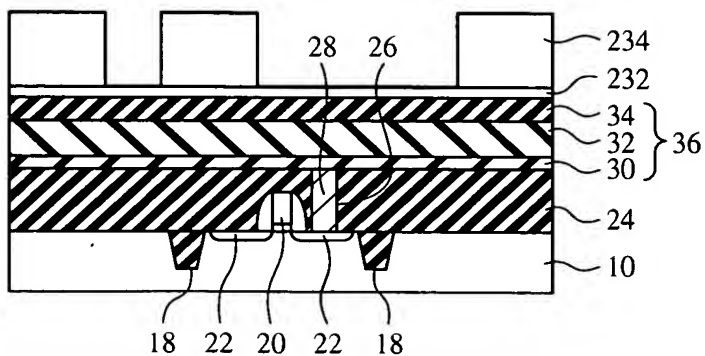


FIG. 3E



4/17

FIG. 4A

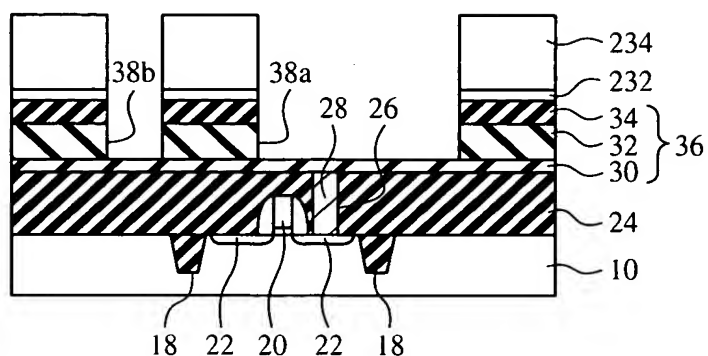


FIG. 4B

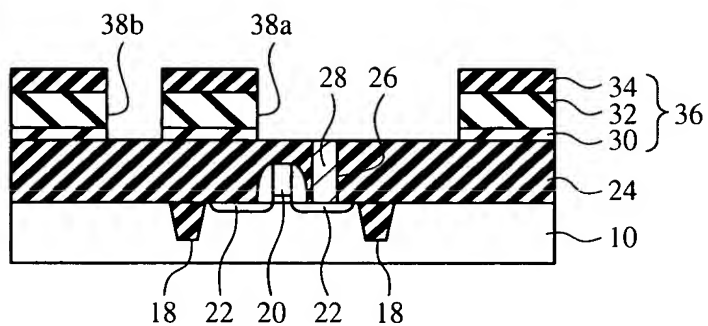


FIG. 4C

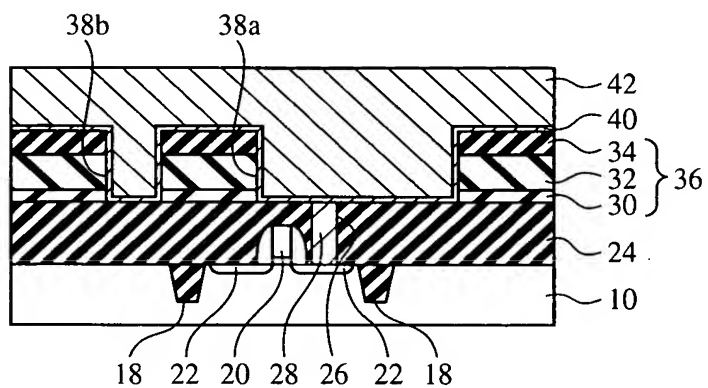


FIG. 4D

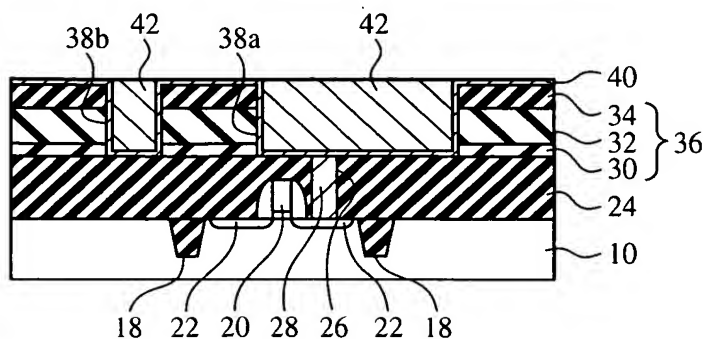


Fig. 1 is a cross-sectional view of a semiconductor device. The device includes a substrate 10. A gate stack is formed on the substrate, comprising layers 18, 20, 22, 26, 28, 30, 32, 34, 36, 38a, 38b, 40, 42, 44a, 44b, 46, 48, 50, 52, 54, 56, 236, and 238. The gate stack is divided into regions 36 and 44a. The regions 36 and 44a are separated by a trench 24. The trench 24 is filled with a material 26. The gate stack is formed on a layer 10. The gate stack is formed on a layer 10. The gate stack is formed on a layer 10.

[illegible]

FIG. 6A

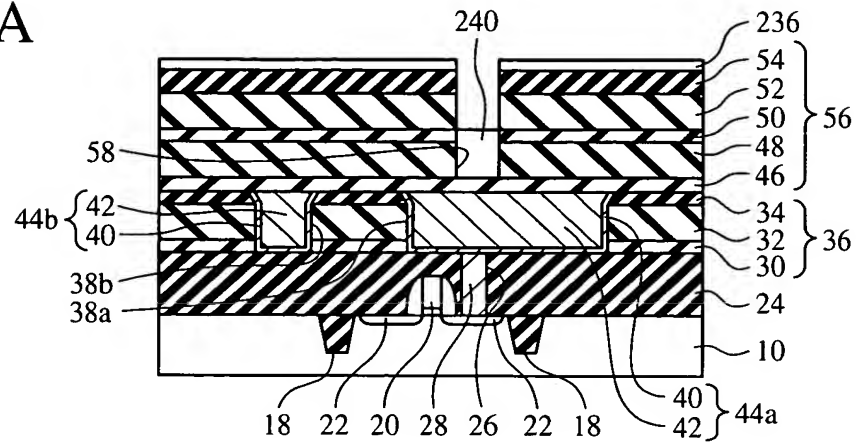


FIG. 6B

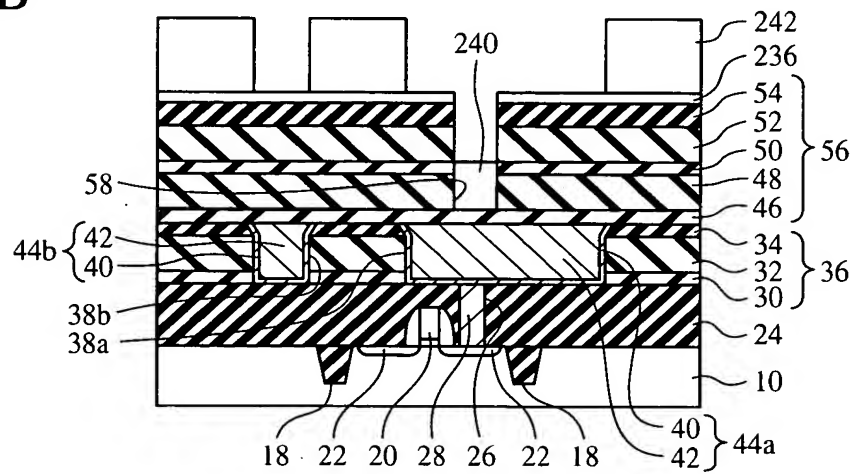
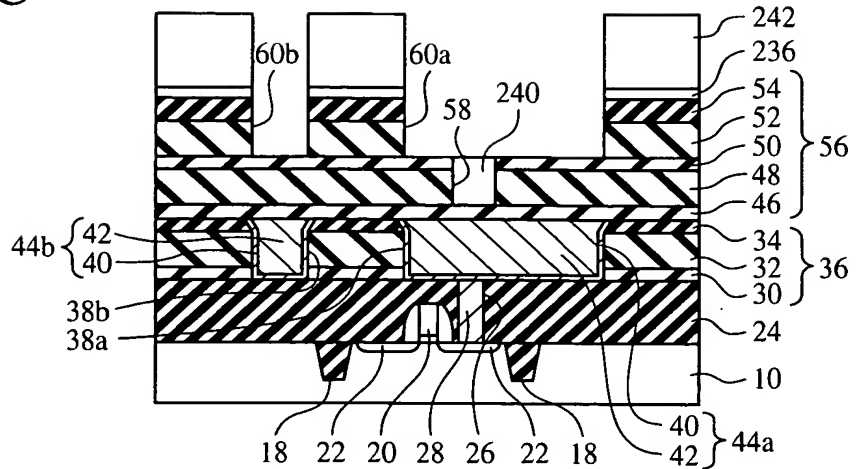


FIG. 6C



[illegible]

Figure B is a cross-sectional view of a semiconductor device. It shows a substrate 10 with a series of layers and structures. At the bottom, there are contact pads 18 and 22, and a layer 24. Above this is a layer 30, which contains a series of rectangular openings 32 and 34. A layer 36 is deposited over the openings 32 and 34, and a layer 38a is deposited over the layer 36. A layer 38b is deposited over the layer 38a. A layer 40 is deposited over the layer 38b, and a layer 42 is deposited over the layer 40. A layer 44a is deposited over the layer 42, and a layer 44b is deposited over the layer 44a. A layer 46 is deposited over the layer 44b, and a layer 48 is deposited over the layer 46. A layer 50 is deposited over the layer 48, and a layer 52 is deposited over the layer 50. A layer 54 is deposited over the layer 52, and a layer 62 is deposited over the layer 54. A layer 64 is deposited over the layer 62. The device includes a series of rectangular openings 60a and 60b, which are filled with a material 60. The openings 60a and 60b are located in the layers 40, 42, 44a, 44b, 46, 48, 50, 52, 54, 62, and 64. The openings 60a and 60b are located in the layers 40, 42, 44a, 44b, 46, 48, 50, 52, 54, 62, and 64. The openings 60a and 60b are located in the layers 40, 42, 44a, 44b, 46, 48, 50, 52, 54, 62, and 64.

8/17

FIG. 8A

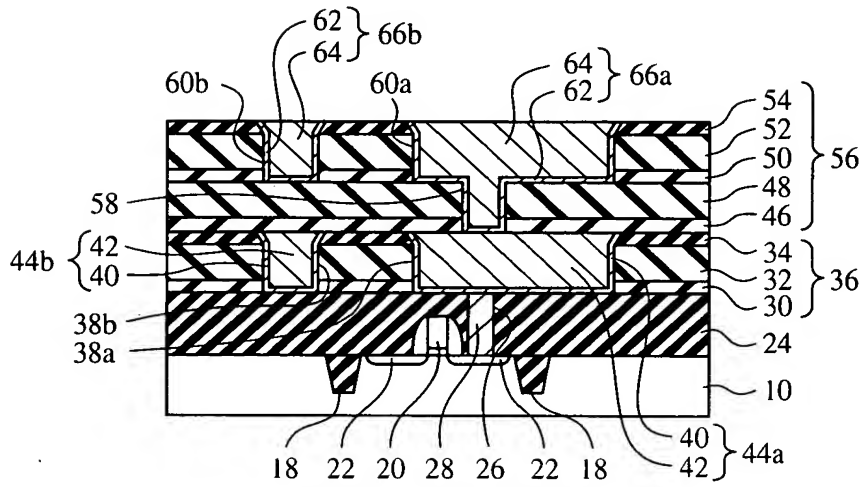
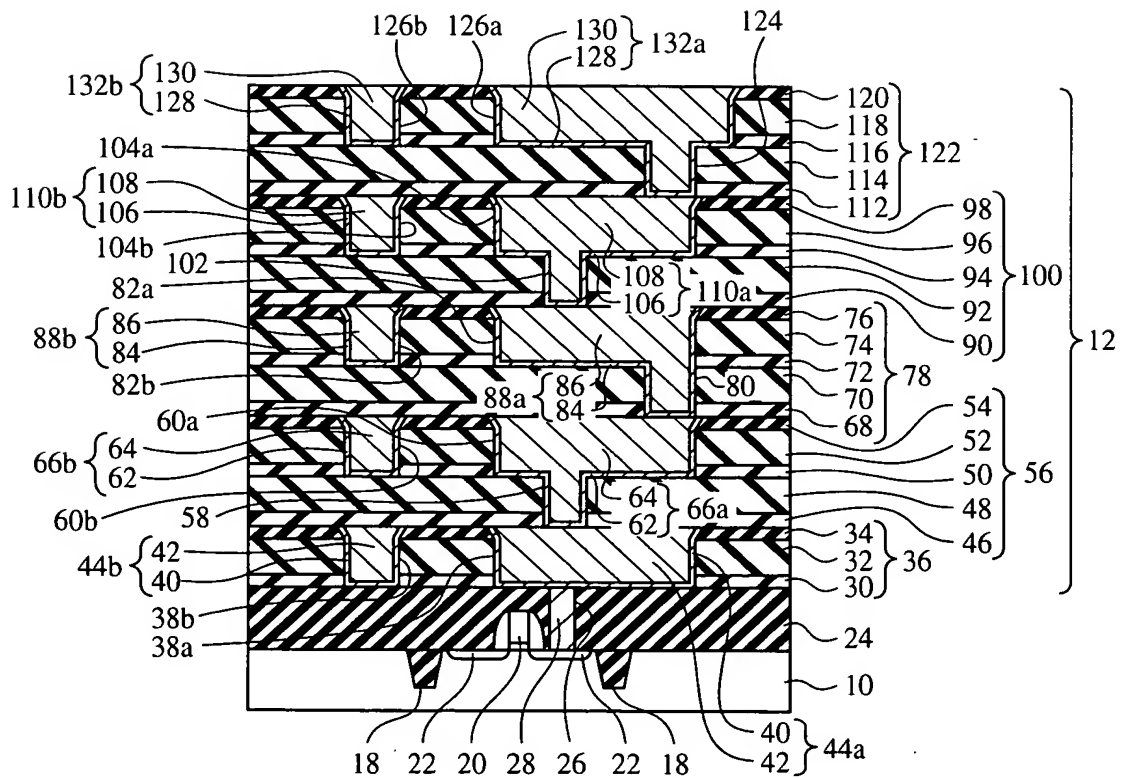


FIG. 8B



9/17

FIG. 9A

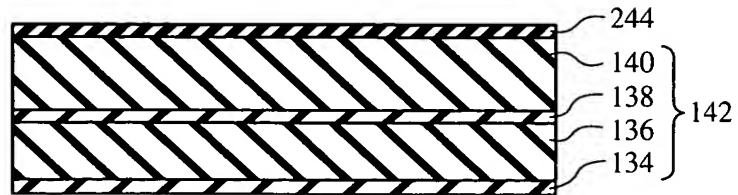


FIG. 9B

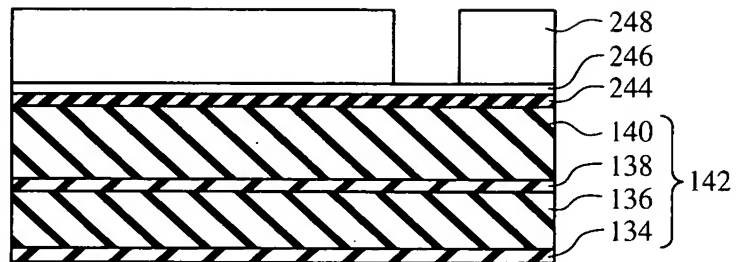


FIG. 9C

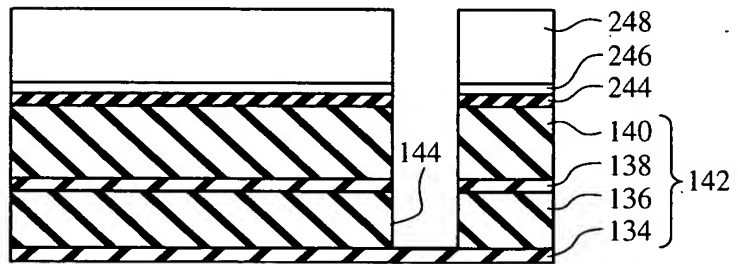
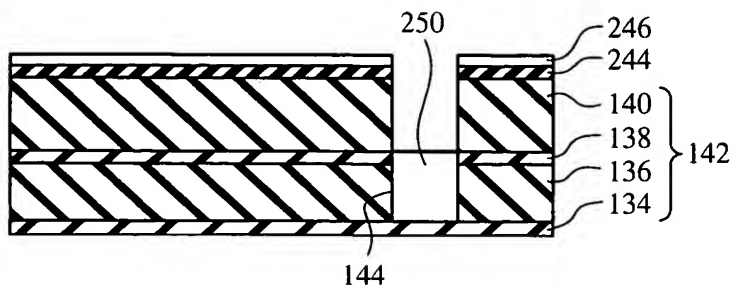


FIG. 9D



10/17

FIG. 10A

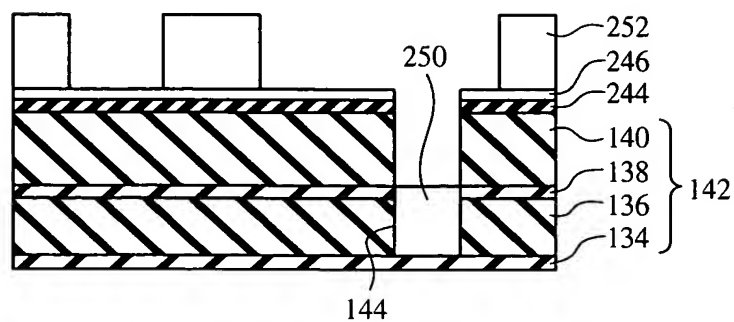


FIG. 10B

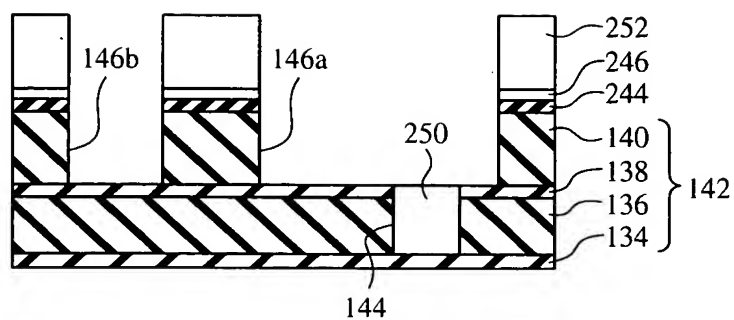


FIG. 10C

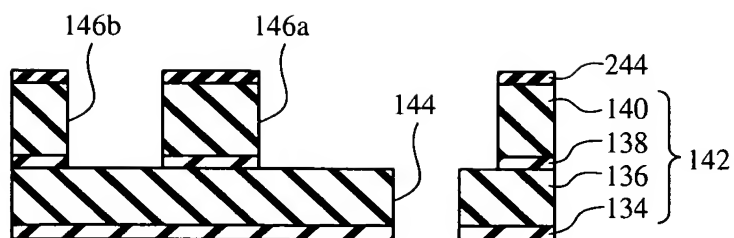
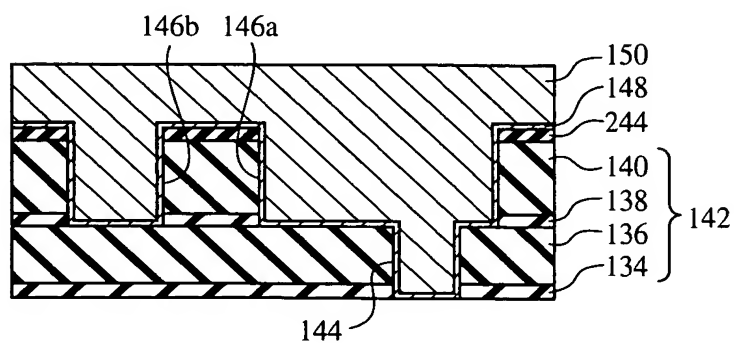


FIG. 10D



11/17

FIG. 11A

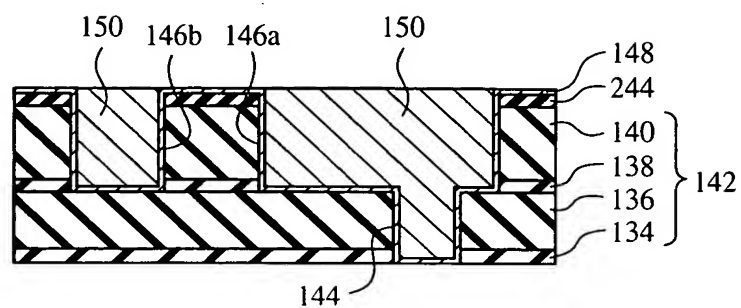


FIG. 11B

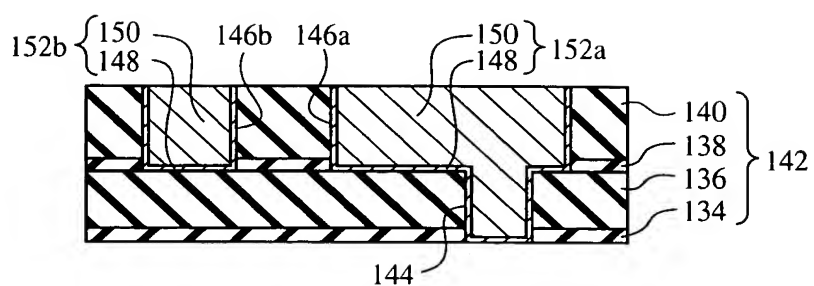
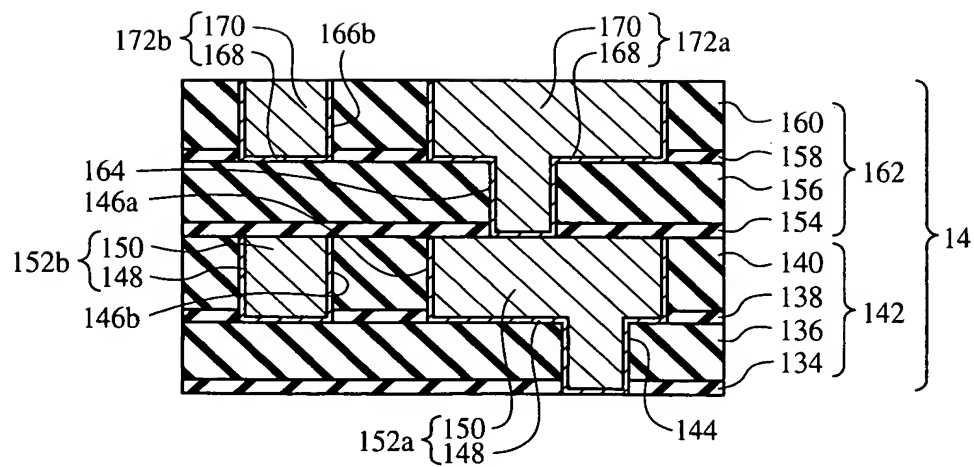


FIG. 11C



12/17

FIG. 12A

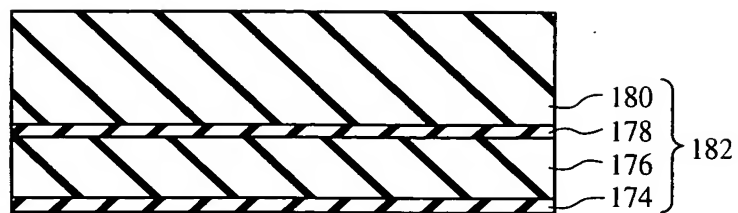


FIG. 12B

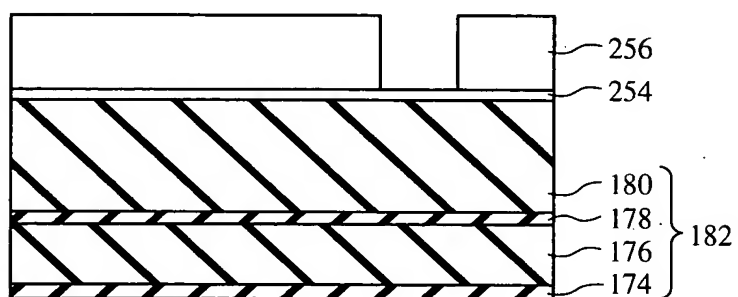


FIG. 12C

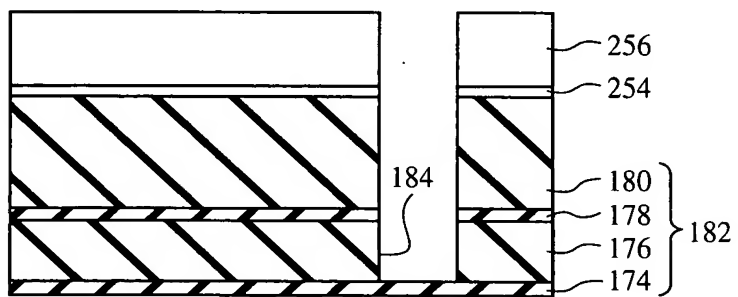
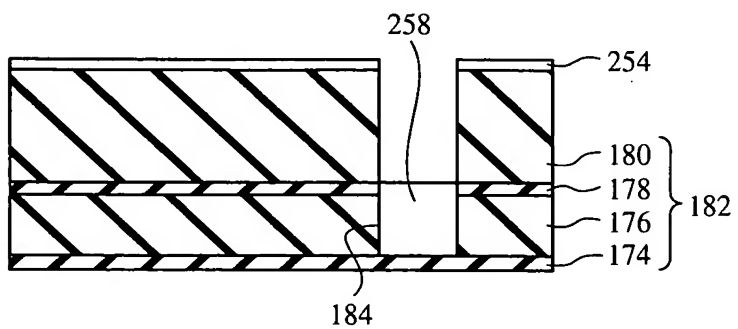


FIG. 12D



13/17

FIG. 13A

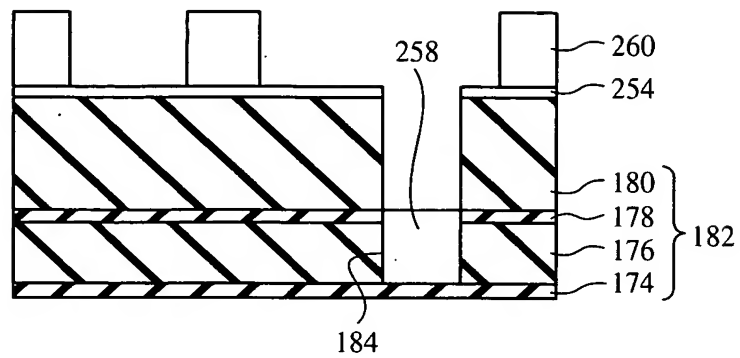


FIG. 13B

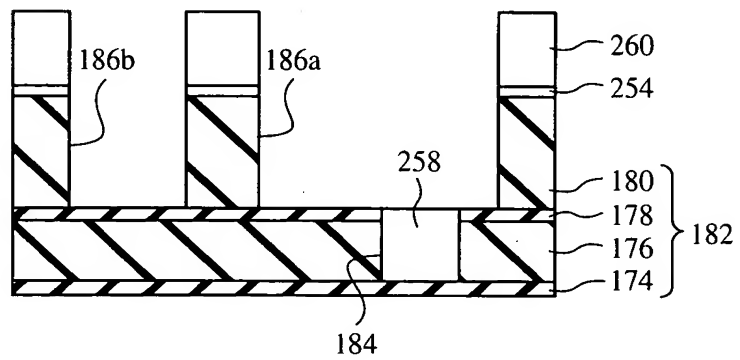


FIG. 13C

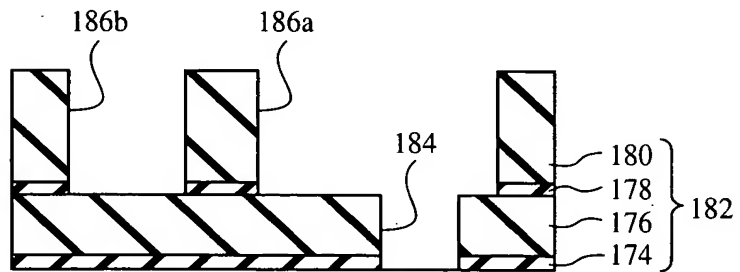
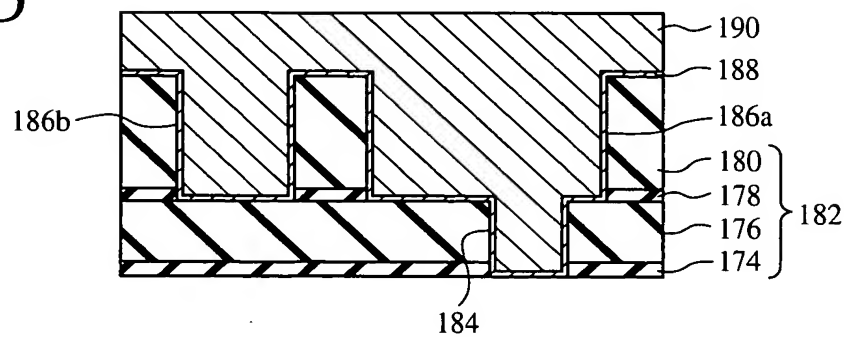


FIG. 13D



14/17

FIG. 14A

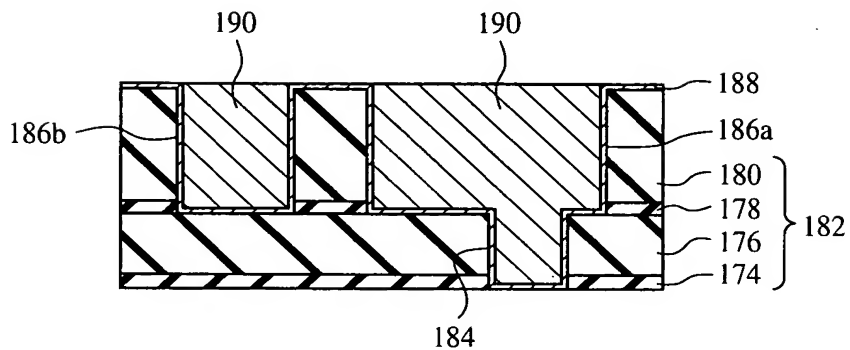


FIG. 14B

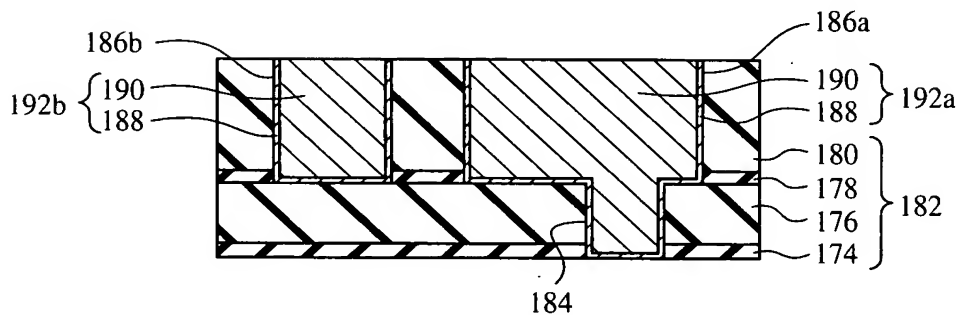
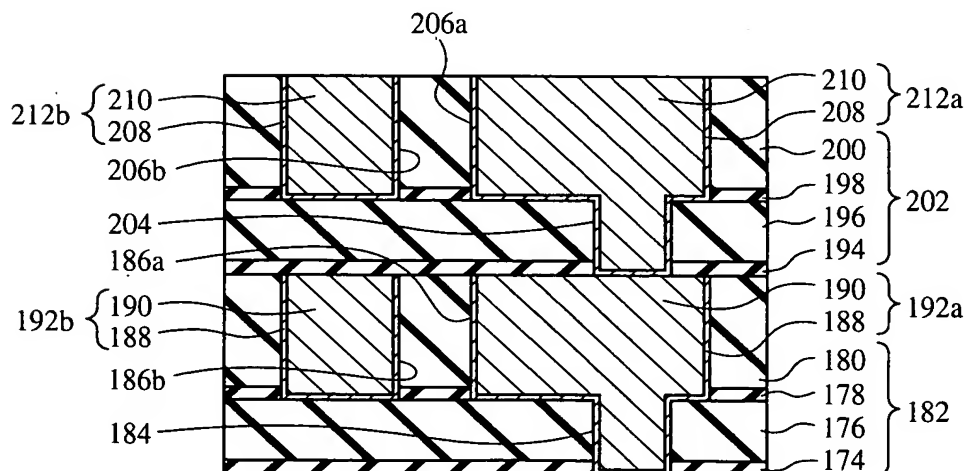


FIG. 14C



15/17

FIG. 15A

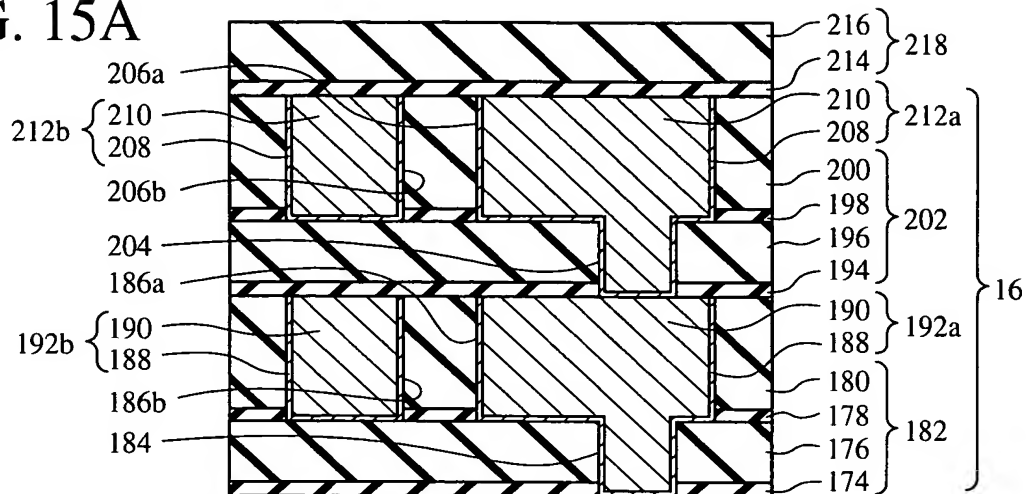


FIG. 15B

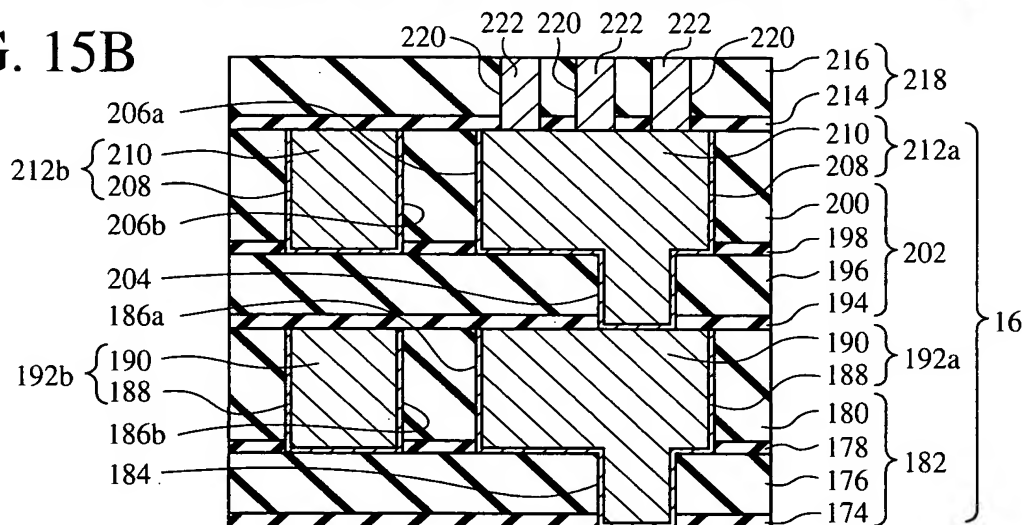
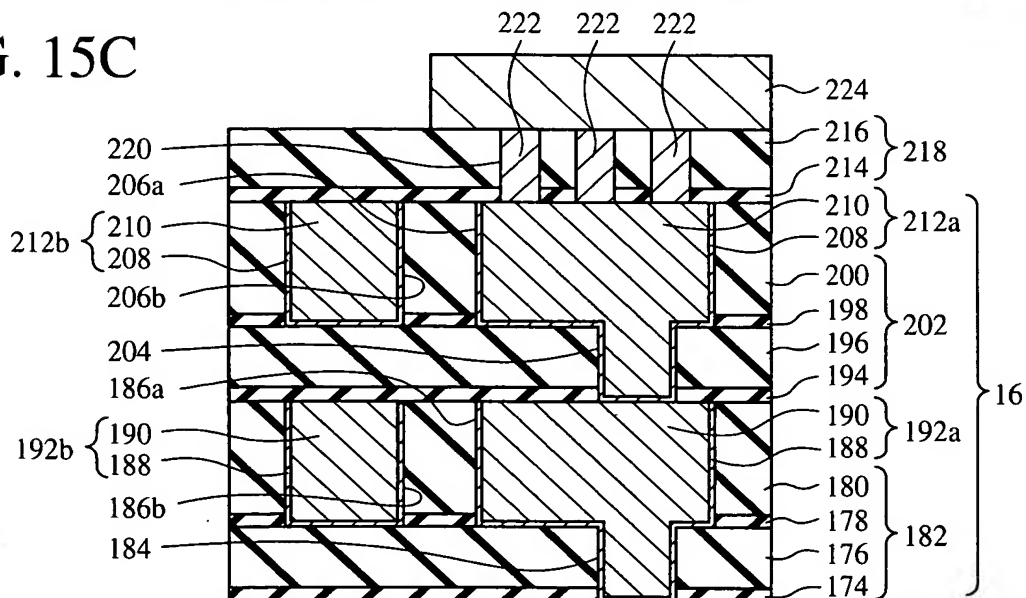


FIG. 15C



16/17

FIG. 16A

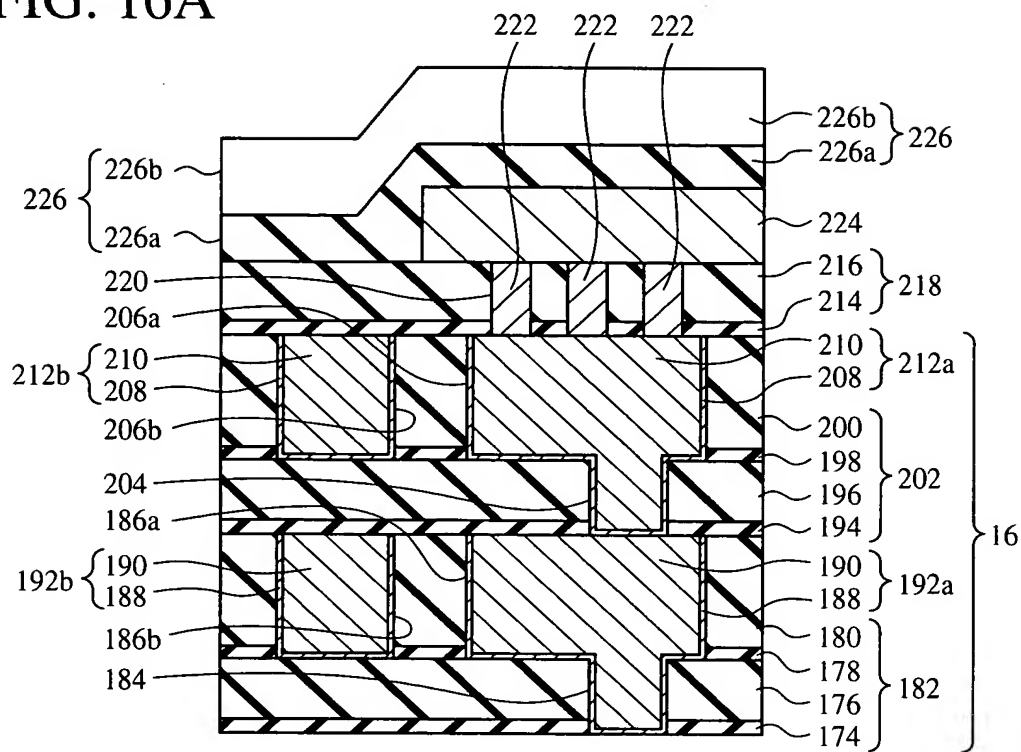
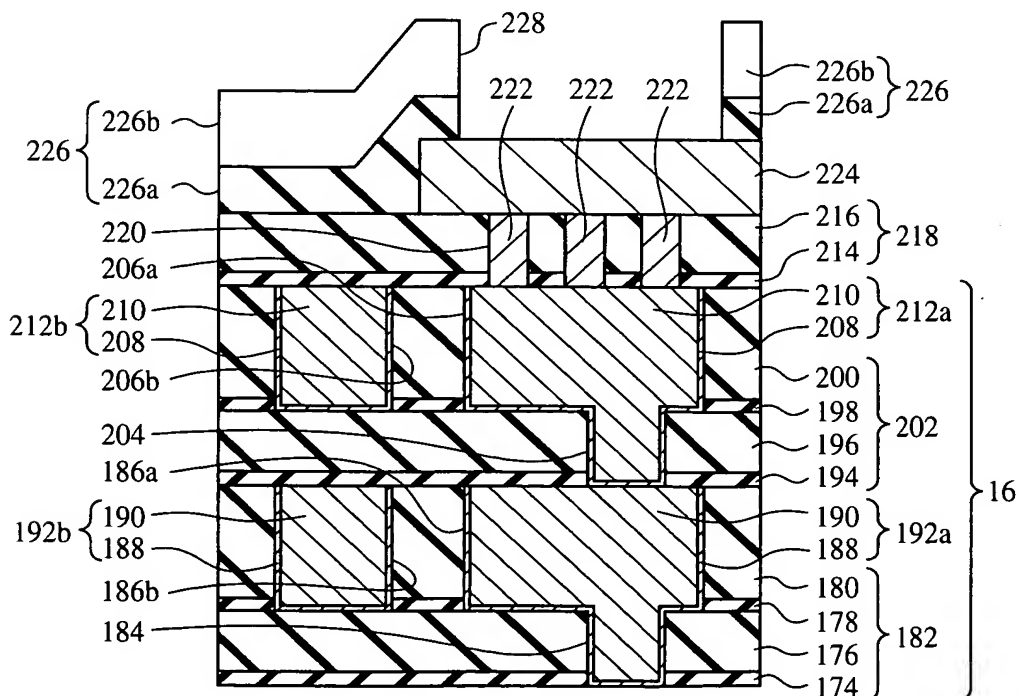


FIG. 16B



17/17

FIG. 17
PRIOR ART

